

WEST[Help](#)[Logout](#)[Interrupt](#)[Main Menu](#)[Search Form](#)[Posting Counts](#)[Show S Numbers](#)[Edit S Numbers](#)[Preferences](#)**Search Results -**

Terms	Documents
L22 and pulses	11

Database:

US Patents Full-Text Database	▲
JPO Abstracts Database	
EPO Abstracts Database	
Derwent World Patents Index	
IBM Technical Disclosure Bulletins	▼

Refine Search:

L22 and pulses

[Clear](#)**Search History****Today's Date: 3/7/2001**

<u>DB Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
USPT	L22 and pulses	11	<u>L24</u>
USPT	L22 and clock pulses	0	<u>L23</u>
USPT	L21 and diode	14	<u>L22</u>
USPT	L20 and L1	14	<u>L21</u>
USPT	L19 and telephone	46	<u>L20</u>
USPT	L18 and interface	297	<u>L19</u>
USPT	L17 and differential	782	<u>L18</u>
USPT	charge pump\$1 and clock\$1 and capacitor\$1	2008	<u>L17</u>
USPT	L10 and differential same capacitively coupled	0	<u>L16</u>
USPT	L10 and differential capacitively coupled	0	<u>L15</u>
USPT	L10 and differentially capacitively coupled	0	<u>L14</u>
USPT	L10 and differentially with capacitively coupled	0	<u>L13</u>
USPT	L10 and differential\$2	11	<u>L12</u>
USPT	L9 and L10	0	<u>L11</u>
USPT	L4 and charge pump	14	<u>L10</u>
USPT	L8 and ((digital signal processor) or DSP)	48	<u>L9</u>
USPT	L5 and diode\$1	104	<u>L8</u>
USPT	L5 and diode1	0	<u>L7</u>
USPT	L5 and diode41	0	<u>L6</u>
USPT	L4 and clock\$1	130	<u>L5</u>
USPT	((data access arrangement) or DAA) and capacitor\$1	217	<u>L4</u>
USPT	L1 and digital data	19	<u>L3</u>
USPT	L1 and capacitive interface\$1	3	<u>L2</u>
USPT	((high voltage) or (voltage) or (electric) or (digital)) isolation barrier\$1	40	<u>L1</u>